

Docket Number: 011765-0304367
Client Reference: AF/JG/P9094US

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

FORIN UDREA

Group Art Unit: 2812

Application No.: 10/602,065

Examiner: UNKNOWN

Filed: June 24, 2003

Confirmation No.: 1160

For: LATERAL SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

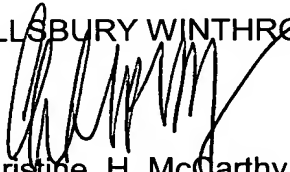
Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, AND (b) before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

Respectfully Submitted,

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FORM PTO-1449 (modified)
To: U.S. Department of Commerce
(PW FORM PAT-1449)
Patent and Trademark Office

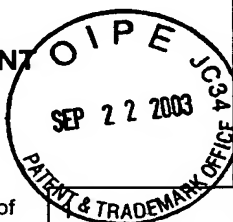
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M#

304367

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**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**



Applicant: Florin UDREA

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of

Examiner: UNKNOWN

Group Art Unit: 2812

U.S. PATENT DOCUMENTS

Examiner's Initials*		Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR	2002/0041003	04/2002	UDREA et al.			
	BR						
	CR						
	DR						
	ER						
	FR						
	GR						
	HR						
	IR						
	JR						
	KR						
	LR						
	MR						
	NR						

FOREIGN PATENT DOCUMENTS

ORIGINAL PAPER DOCUMENTS							Abstract		Readily Available	
		Document Number	Date MM/YYYY	Country	Inventor Name		Enclosed	No	Enclose	No
	OR	WO 02/25700A2	03/2002	PCT	UDREA et al.					
	PR									
	QR									
	RR									
	SR									
	TR									
	UR									
	VR									

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

	WR	Udrea et al., "3D RESURF double-gate MOSFET: A revolutionary power device concept," <i>Electronics Letters</i> 34(8):808-809 (1998)			
	XR				
	YR				
	ZR				
	AAR				
	BBR				
	CCR				

Examiner

Date Considered:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.